

Application No. 09/933,847
Amendment dated February 2, 2005
Reply to Office action of August 2, 2004

AMMENDMENTS TO THE ABSTRACT

Please replace the Abstract with the following rewritten Abstract.

The present invention is a technique to perform field operations. A mask generator generates a mask field for an operand having a word length. The mask field defines an operand field within the operand to be operated by the operation. The operand field has a field length. The execution unit executes the operation on the operand field.

A microprocessor capable of performing operations on arbitrary bit-fields within operands. These operands and the delimitation of the bit field may be specified by a single instruction. Operations may cause condition codes to be set based directly on the condition of the bit-field result, and therefore affect the flow of the program executing on the microprocessor. These operations take the place of multiple insert, extract, and word-wise operations in a conventional microprocessor.